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Laterally defined freely suspended quantum dots in GaAs/AlGaAs heterostructures

C Rossler¹, M Bichler², D Schuh³, W Wegscheider³ and S Ludwig¹

¹ Center for NanoScience and Fakultät für Physik der Ludwig-Maximilians-Universität,

Geschwister-Scholl-Platz 1, D-80539 Munich, Germany

² Walter Schottky Institut, Technical University Munich, Am Coulombwall 3, D-85748

Garching, Germany

³ Institut für Angewandte und Experimentelle Physik II, University Regensburg,

Universitätsstrasse 31, D-93040 Regensburg, Germany

E-mail: roessler@lmu.de

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Abstract

Free-standing beams containing a two-dimensional electron system are shaped from a GaAs/AlGaAs heterostructure. Quantum point contacts and (double) quantum dots are laterally defined using metal top gates. We investigate the electronic properties of these nanostructures by transport spectroscopy. Tunable localized electron states in freely suspended nanostructures are a promising tool to investigate the electron–phonon interaction.

(Some figures in this article are in colour only in the electronic version)

1. Introduction

The coherent dynamics of charge states in solid-state-based quantum dots (QDs) is limited at typical experimental temperatures by the electron-phonon interaction [1]. The phonon emission spectrum of a detuned double QD in bulk GaAs has been investigated by measuring the inelastic tunnel Double QDs that are defined in a freely current [2]. suspended bridge are subject to a phonon spectrum that is modified by the cavity modes of the bridge. Therefore, the inelastic tunnel current of a freely suspended double QD will allow a systematic study of the cavity phonon spectrum and its interaction with the electronic states of the QDs. The fabrication of a freely suspended bridge containing metal top gates designed to define a double QD has been reported [3]. A coherent interaction between cavity phonon modes and localized electron states in a QD defined by etched narrow constrictions in a freely suspended beam has been investigated [4]. Here we report on systematic studies of the electronic transport properties of laterally defined suspended QDs. In these devices the charge states are widely tunable by using metal gates. In particular, we study the influence of metal top gates on the electronic stability of freely suspended devices.

2. Experimental details

The GaAs/Al_{0.3}Ga_{0.7}As heterostructures (active layer) of thickness 130 nm (sample I) or 90 nm (sample II and sample III) are separated from the substrate by an AlAs/GaAs superlattice (sacrificial layer). In both heterostructures, the active layer contains a two-dimensional electron gas (2DEG) about 40 nm below the surface⁴. The ohmic contacts situated at non-suspended areas allow electrical connection of the 2DEG. Optical lithography is used to deposit a 100 nm thick layer of nickel to protect the areas of the heterostructure that will later form the 2DEG leads. A 60 nm thick layer of gold is deposited for the contacts of the top gates and alignment marks. In the next step 30 nm thick top gates (5 nm titanium, 25 nm gold) are defined by e-beam lithography. In a second step of ebeam lithography, the intended beam area and its connection to the 2DEG leads is covered by a 100 nm thick layer of nickel. This is done in the last step of lithography because the beam has to be aligned accurately with respect to the gates and the nickel will be removed afterward. Anisotropic reactive ion etching using SiCl₄ removes the uncovered top layers of the

⁴ Non-suspended 2DEG properties of samples I, II: z = 130 nm, $\mu = 411000 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, $n_s = 2.55 \times 10^{11} \text{ cm}^{-2}$ (dark); sample III: z = 90 nm, $n_s = 7.8 \times 10^{11} \text{ cm}^{-2}$ (dark).



Figure 1. (a) Atomic force micrograph of the split gate device sample I. Ten gates (vertical stripes) lie on top of the freely suspended beam (dark horizontal bar), which is 130 nm thick, 600 nm wide and 6 μ m long. (b) Scanning electron micrograph of a freely suspended beam (sample II) recorded at a tilt angle of 85°.



Figure 2. (a) Two-terminal differential conductance after subtraction of the lead resistance as a function of the voltage applied to gates G_1 and G_2 while all other gates are grounded. The three traces are measured at subsequent times after illumination. (b) Differential conductance as a function of time for an ungated reference bridge (top curve) and for different voltages applied to gates G_1 and G_2 .

heterostructure [5]. The nickel layer is then removed by Fe(III) chloride. Finally, isotropic wet etching with 1% hydrofluoric acid selectively removes the 400 nm thick sacrificial layer in the vicinity of the already etched trenches. The remaining free-standing beams are about 6 μ m long and between 500 and 800 nm wide. One of our devices (sample I) is shown in the atomic force micrograph in figure 1(a). The split gate geometry of these gates was designed to laterally define a double QD. The ten vertical stripes (five on each side of the beam) are metal top gates. They are freely suspended between the beam (dark horizontal stripe) and the sockets (dark rectangles at the top and bottom). The scanning electron micrograph of sample II is shown in figure 1(b). This image has been recorded under a tilt angle of 85° to better visualize the freely suspended regions.

The samples were cooled down in a dilution refrigerator with a base temperature of $T_{\rm MC} \cong 20$ mK. The electron temperature has been determined as $T_{\rm el} \leq 60$ mK from Coulomb blockade oscillations of non-suspended QDs on a similar sample. After initial illumination with an infrared LED, two-terminal differential conductance measurements through the beams were performed at a frequency of f = 67 Hz and modulation voltage of $V = 15 \ \mu$ V. The quantized conductance of a quantum point contact (QPC) as a function of identical negative voltages applied to gates G_1 and G_2 (figure 1(a)) is presented in figure 2(a). In these measurements all other gates are grounded. The lead resistance $R_1 = 2850 \ \Omega$ is already subtracted $(1/G = 1/G_{\rm measured} - R_1)$. The value of R_1 (measured two-terminal resistance $R_2 \ \cong 3 \ k\Omega$) has been



Figure 3. (a) Scanning electron micrograph of sample III (top view). The free-standing beam is 4 μ m long, 800 nm wide and 90 nm thick. Three gates G_L , G_C and G_R define quantum dots (dashed circles) and two gates P_L and P_R are used to control the number of electrons in the quantum dot. (b) Coulomb oscillations of the right QD as a function of V_{PR} for $V_{GC} = V_{GR} = -0.275$ V. (c) Grayscale plot of the differential conductance (linear scale) as a function of V_{PR} and source–drain bias V_{SD} . White lines are a guide for the eyes.

chosen so that the conductance plateaus of the first depletion curve (upper curve in figure 2(a)) are quantized in multiples of $2e^2/h$.

3. Results and discussion

While depleting the QPC for the first time (upper curve in figure 2(a)) it shows clear conductance plateaus at $G = 2ne^2/h$; n = 0, 1, 2, ..., 11. A second depletion process (after slowly sweeping the gate voltages back to zero) results in less pronounced conductance plateaus showing irregular shapes and full depletion at a higher gate voltage. Note that the lead resistance is almost unaltered compared to the first depletion curve as the plateaus are still close to $G = 2ne^2/h$. The lowest curve in figure 2(a) was measured five days later after many depletion cycles. Now the QPC is completely closed at even higher gate voltages. The depletion curve shows characteristic resonances (local extrema) pointing to localized electron states [6].

Such a behavior can be explained by electrons that tunnel from the gates into the active layer. A tunnel current between the 2DEG and top gates has previously been reported in a non-suspended heterojunction [7]. Charges tunneling from the surface into the active layer produce reorganizations of the local electrostatic potential causing temporal fluctuations of the conductance as well as permanent depletion. The time dependence of the conductance is shown in figure 2(b) for different gate voltages. The top curve is measured on a suspended beam without gates. It shows no pronounced conductance fluctuations during the time period of 12 min. The lower three curves are measured on the device shown



Figure 4. Linear grayscale plot of the differential conductance of the double QD (figure 3(a)) as a function of the voltages applied to gates $P_{\rm L}$ and $P_{\rm R}$. The voltages applied to the other gates are $V_{\rm GL} = -0.24$ V, $V_{\rm GC} = -0.23$ V and $V_{\rm GR} = -0.23$ V.

in figure 1(b). Fluctuations are more frequent at lower gate voltages. This observation supports the model of electrons tunneling from the gates into the active layer. For a more negative gate voltage the electric field between the gates and the 2DEG increases and thereby decreases the tunnel barrier, allowing a larger tunneling current of electrons into the active layer [7]. Note that the amplitude of the conductance fluctuations shown in figure 2(b) depends on the strongly varying slope of the depletion curve and is a non-monotonic function of the gate voltage. We were not able to define a double QD with this device because the gate voltages necessary to create tunnel barriers result in critical leakage currents and an unstable background potential.

A revised gate design is depicted in figure 3(a). In contrast to the other two samples three gates G_L , G_C and G_R completely cross the bridge. The gates define tunnel barriers of the two serial QDs (dashed circles). Two gates $P_{\rm L}$ and $P_{\rm R}$ (plunger gates) control the number of electrons in the QDs. The advantage of gates crossing the whole beam (figure 3(a)) compared to the split gate design used before (figure 1) is that less negative voltages are needed for depletion. Therefore, we expect smaller leakage currents for the Schottky gates and a more stable device. We find that a QD is formed at a voltage $V_{\rm G} = -0.275$ V applied to gates $G_{\rm C}$ and $G_{\rm R}$, where the conductance exhibits Coulomb blockade oscillations as a function of the voltage applied to gate $P_{\rm R}$ (see figure 3(b)). In figure 3(c) the conductance is plotted using a linear grayscale as a function of the plunger gate voltage $V_{\rm PR}$ and the sourcedrain voltage V_{SD} . Coulomb blockade appears as darker regions (lower conductance) in the transport spectrum. From the charging energy $E_{\rm c} \sim 0.8$ meV and the dielectric constant of GaAs ε_{GaAs} ~ 13 we estimate the diameter of the QD to be $e^2/4\varepsilon_{\text{GaAs}}\varepsilon_0 E_{\text{c}} \sim d \sim 400$ nm. A very similar QD could be defined between gates G_L and G_C while grounding $G_{\rm R}$ (data not shown). The estimated diameters of the QDs are in excellent agreement with the sample geometry (figure 3(a)). The gate voltage at which each Coulomb blockade oscillation

occurs varies over time by $\Delta V_{\rm G} \sim \pm 1$ meV. This corresponds to a change of the local electrostatic potential of $\Delta \phi \sim$ 400 μ eV which is larger than the phonon cavity energies of $\varepsilon_{\rm ph} \sim 100 \ \mu$ eV expected from numerical calculations [8]. Thus, quantized mechanical excitations cannot yet be resolved.

Figure 4 shows the conductance as a function of both $V_{\rm PL}$ and $V_{\rm PR}$ while applying voltages $V_{\rm GL} = -0.24$ V, $V_{\rm GC}$ = -0.23 V and $V_{\rm GR}$ = -0.23 V to the tunnel barrier gates. Coulomb blockade appears as dark regions (low conductance) in the energy diagram. Bright lines of high conductance exhibit a nonlinear dependence on both plunger gates, typical for the stability diagram of a double QD [9]. Black lines emphasize the two apparent slopes of these resonances, each corresponding to the charging line of one of the two QDs. The fact that the triple points (where the bright lines of two slopes meet) are connected by (bright) lines of high differential conductance indicates that the two QDs are strongly tunnel coupled [10]. The characteristics of a weakly coupled DQD were not observable, probably because the resulting lower tunnel current has been obscured by the apparent conductance fluctuations.

4. Conclusion

In summary, we have investigated freely suspended quantum point contacts and (double) quantum dots defined by split gates. Our studies reveal that the fabricated top gated devices lack the electronic stability needed to employ them for detailed investigations of electronic transport through single and coupled quantum dots on suspended beams. Fluctuations induced at larger gate bias are most likely caused by tunneling of electrons from the gate into the 2DEG. A revised design employed gates that completely cross the bridge in order to reduce the tunnel currents and was successfully used to define a double quantum dot. Still, the fluctuations apparent in this system prevent a systematic investigation of electron– phonon interactions. In future, etched side gates might provide a way to avoid switching noise due to leakage currents while maintaining full tunability of the system.

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